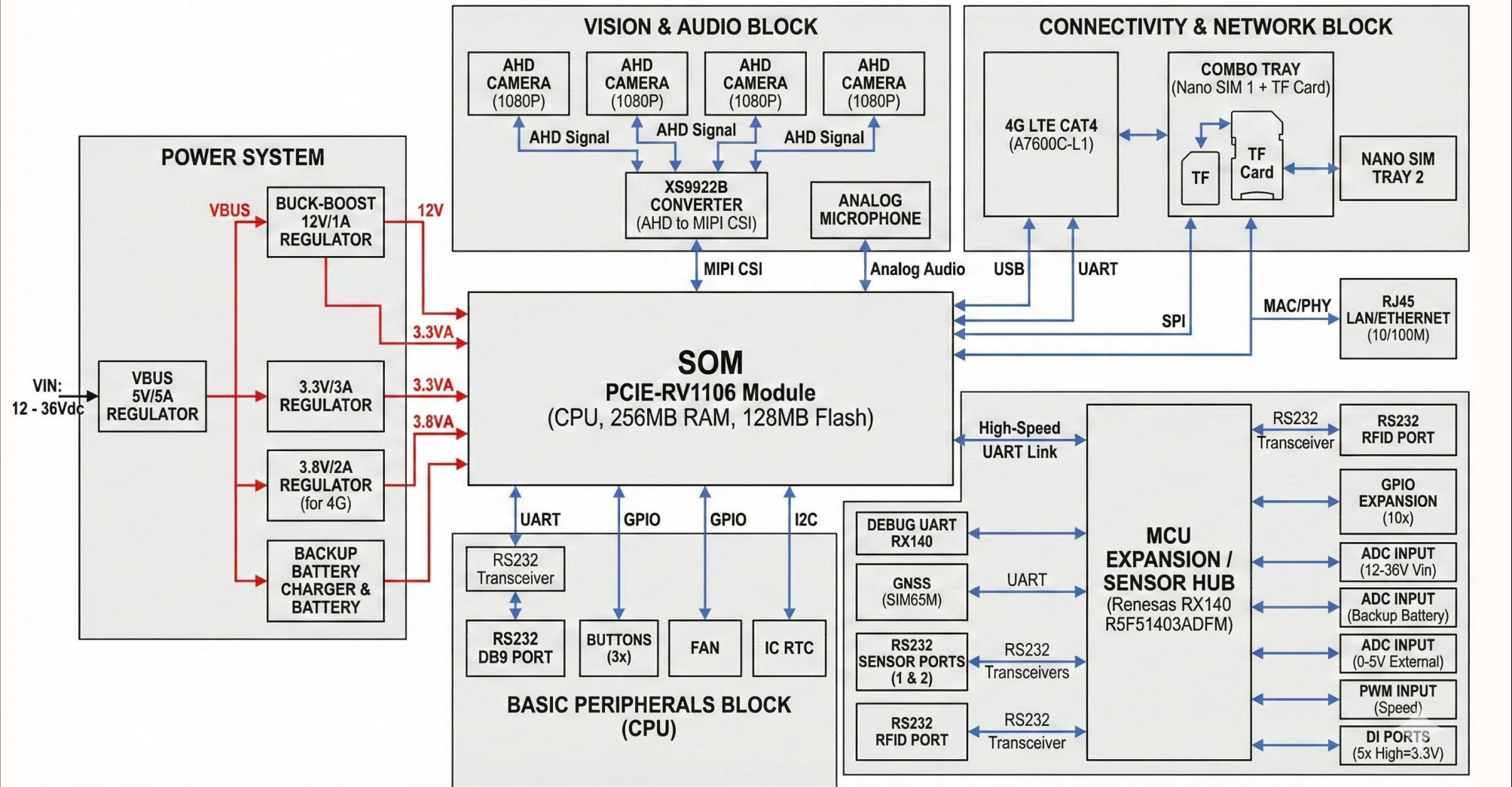
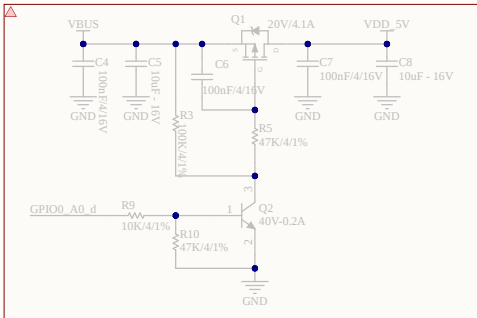


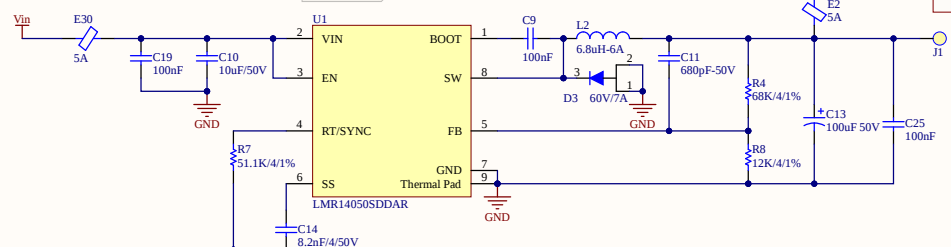
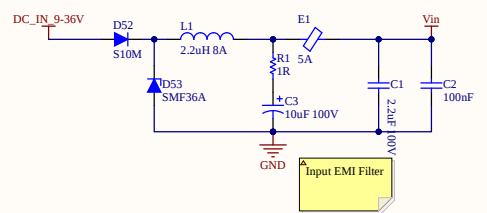
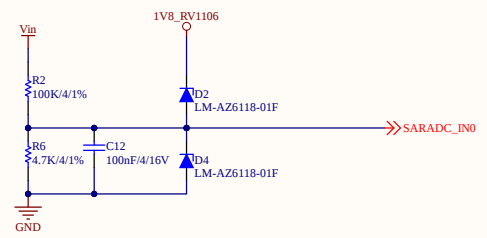


Title		
Size	Number	Revision
A4		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\Block_Diagram.SchDoc	Drawn By:

Đã CHECK OK
ngày 26/02/2026

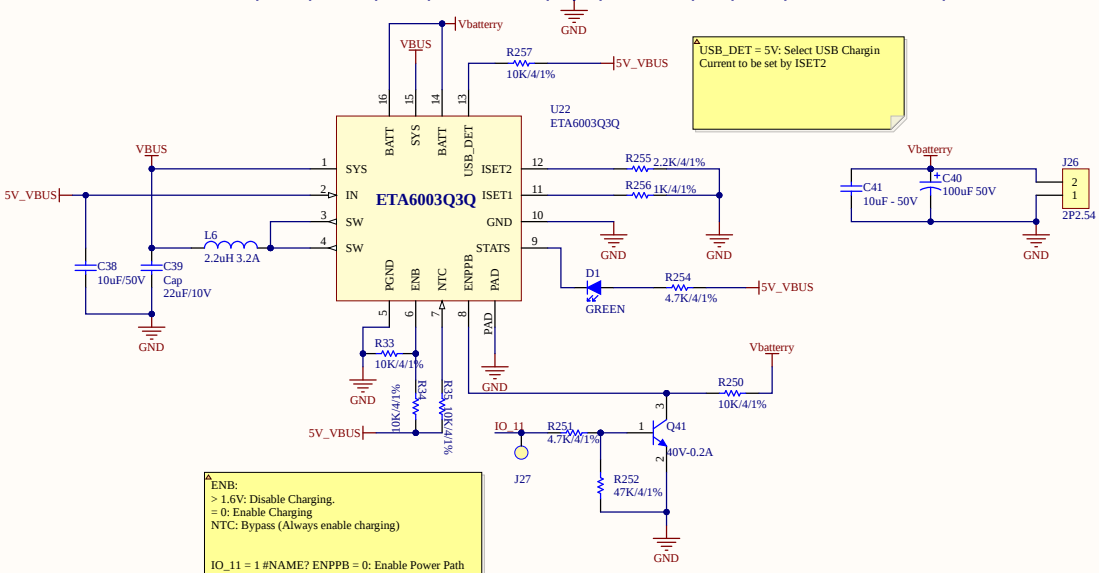
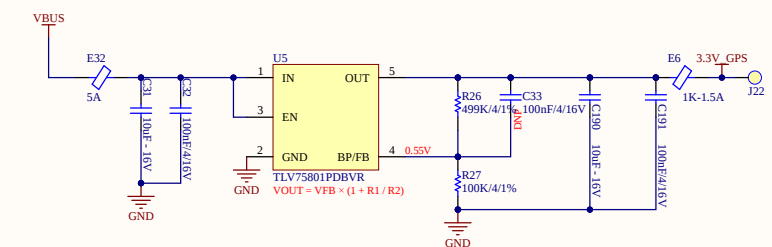
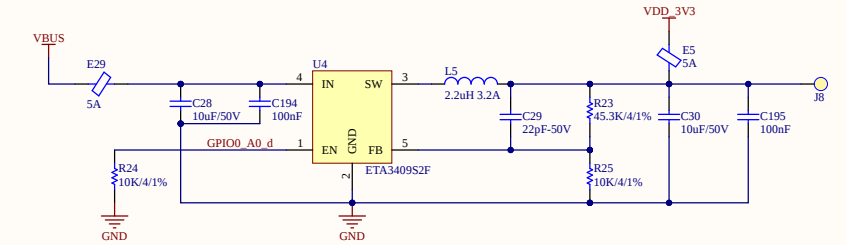
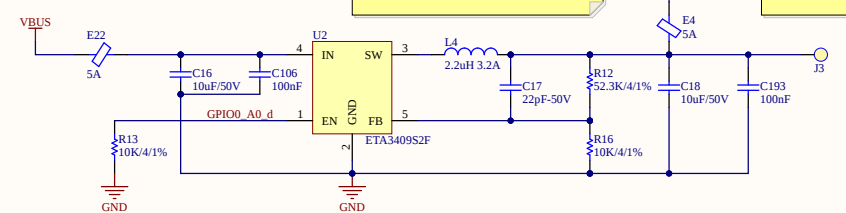


Check Vin



Sửa thành nguồn 3.8V/3A

R12 dùng trở 52.3kOhm 0402. R12
sửa từ mã gốc là R23: 45.3kOhm nên
có thể bị sai khi xuất BOM, do chưa
update thư viện riêng cho trở
52.3kOhm



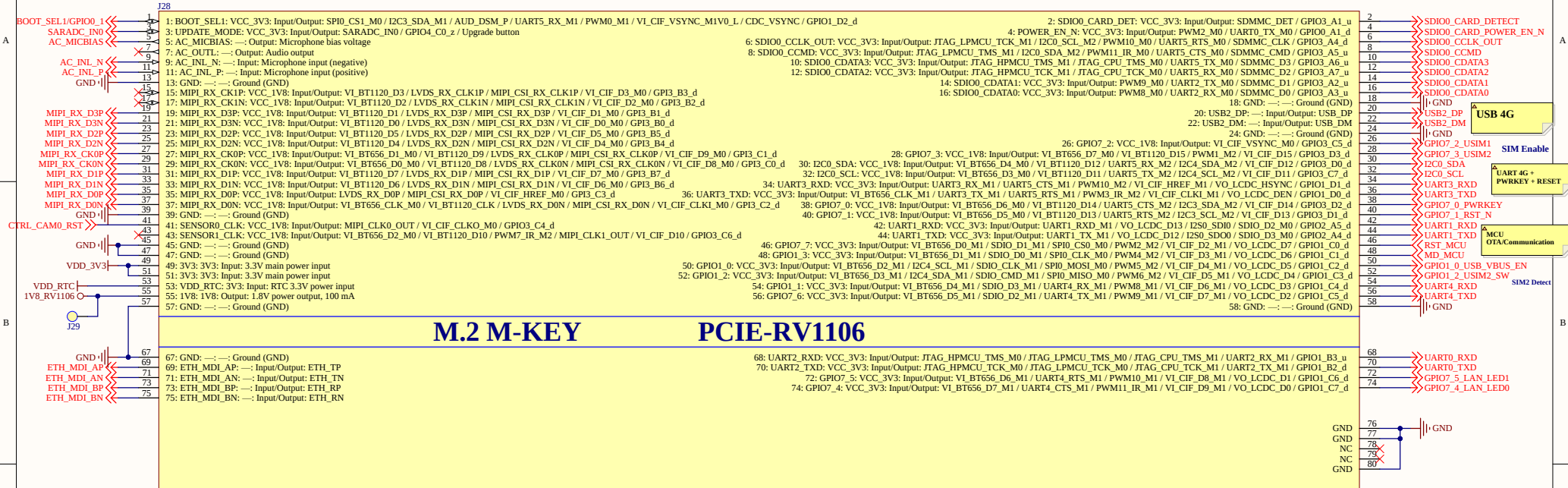
USB_DET = 5V: Select USB Charging
Current to be set by ISET2

ENB:
> 1.6V: Disable Charging.
= 0: Enable Charging
NTC: Bypass (Always enable charging)

IO_11 = 1 #NAME? ENPPB = 0: Enable Power Path
from BATT to SYS
IO_11 = 0 #NAME? ENPPB = 1: Disable Power Path
from BATT to SYS

Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\POWER.SchDoc	Drawn By:

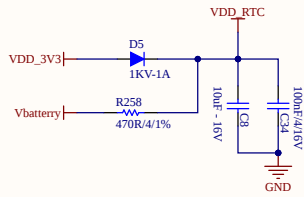
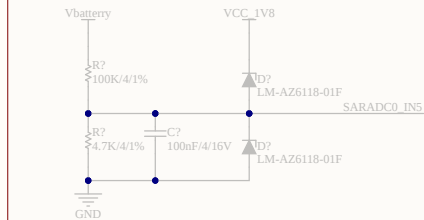
Đã CHECK OK
ngày 26/02/2026



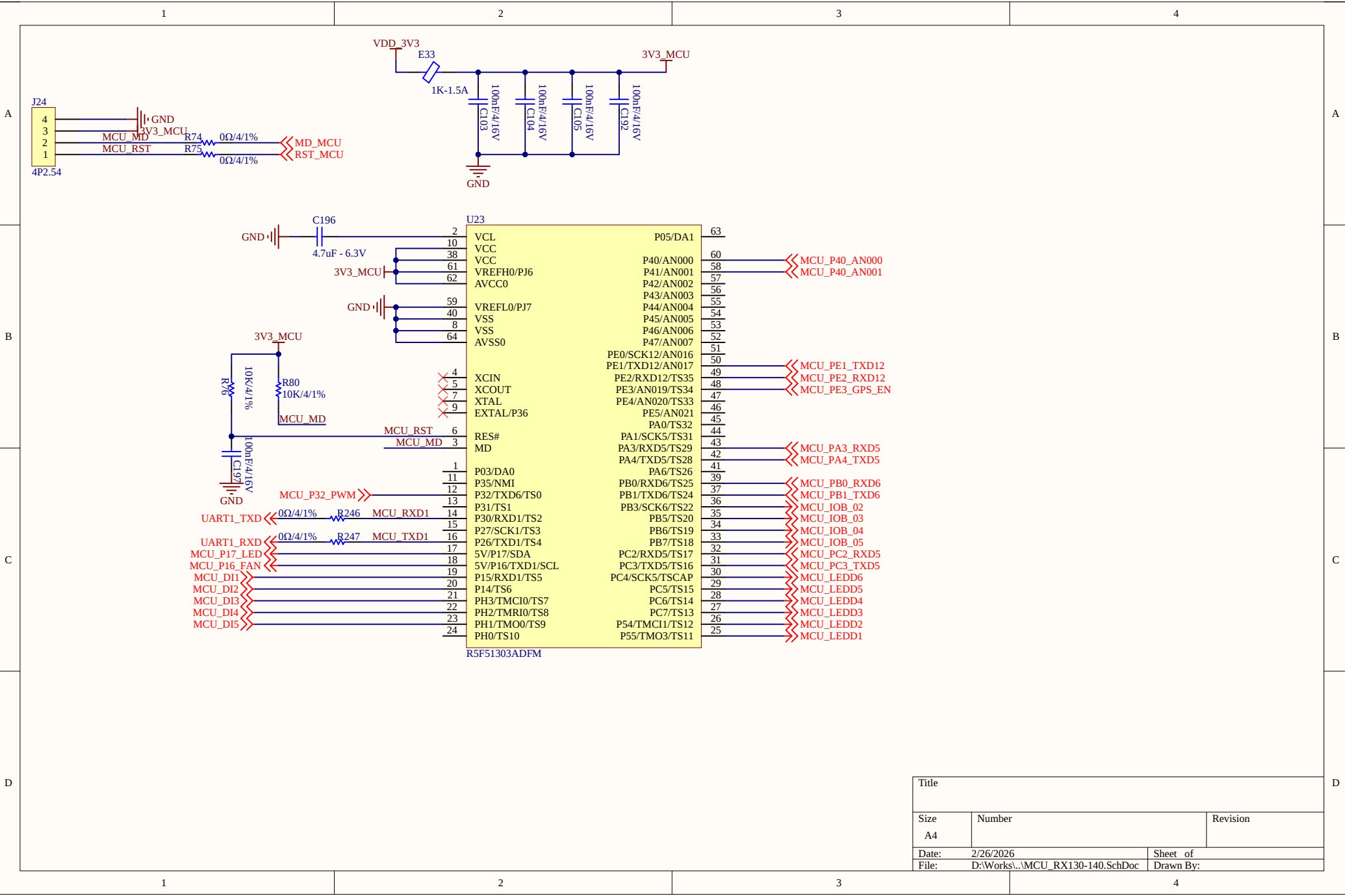
M.2 M-KEY

PCIE-RV1106

Check Vbat

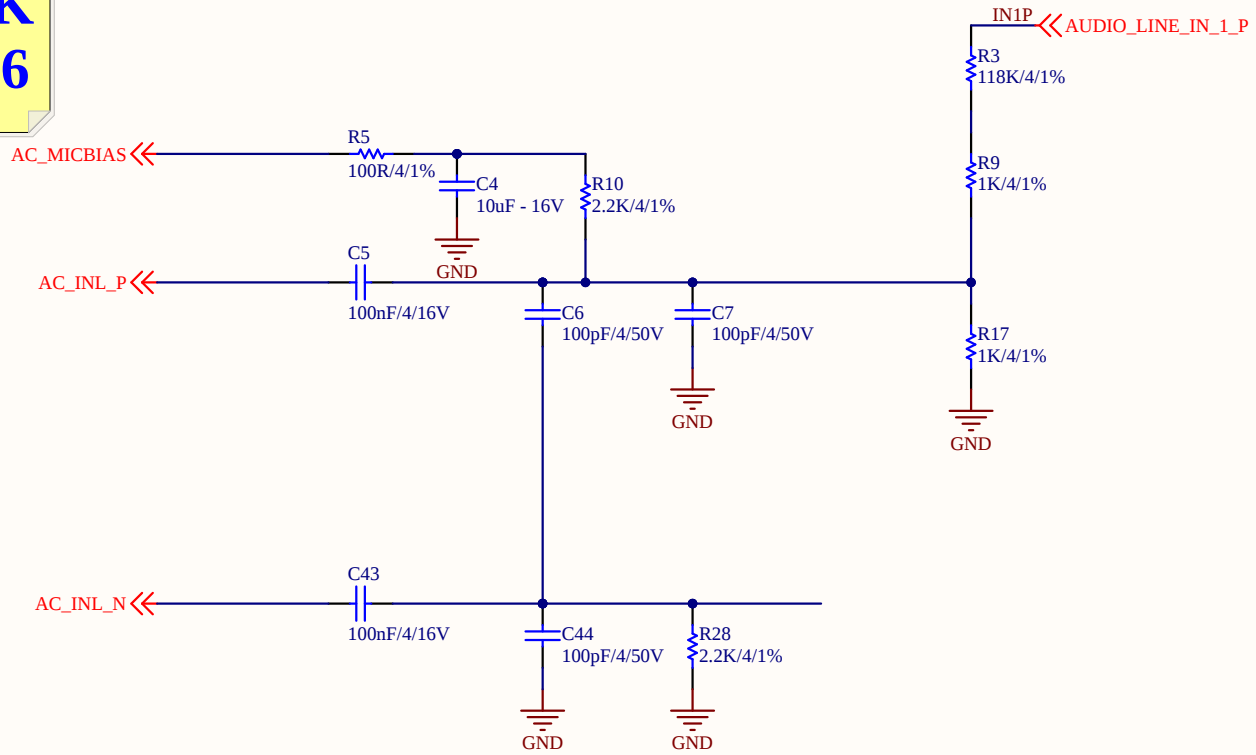


Title			
Size	Number	Revision	
Date:	2/26/2026	Sheet of	
File:	D:\Works\...CORE RV1106.SchDoc	Drawn By:	



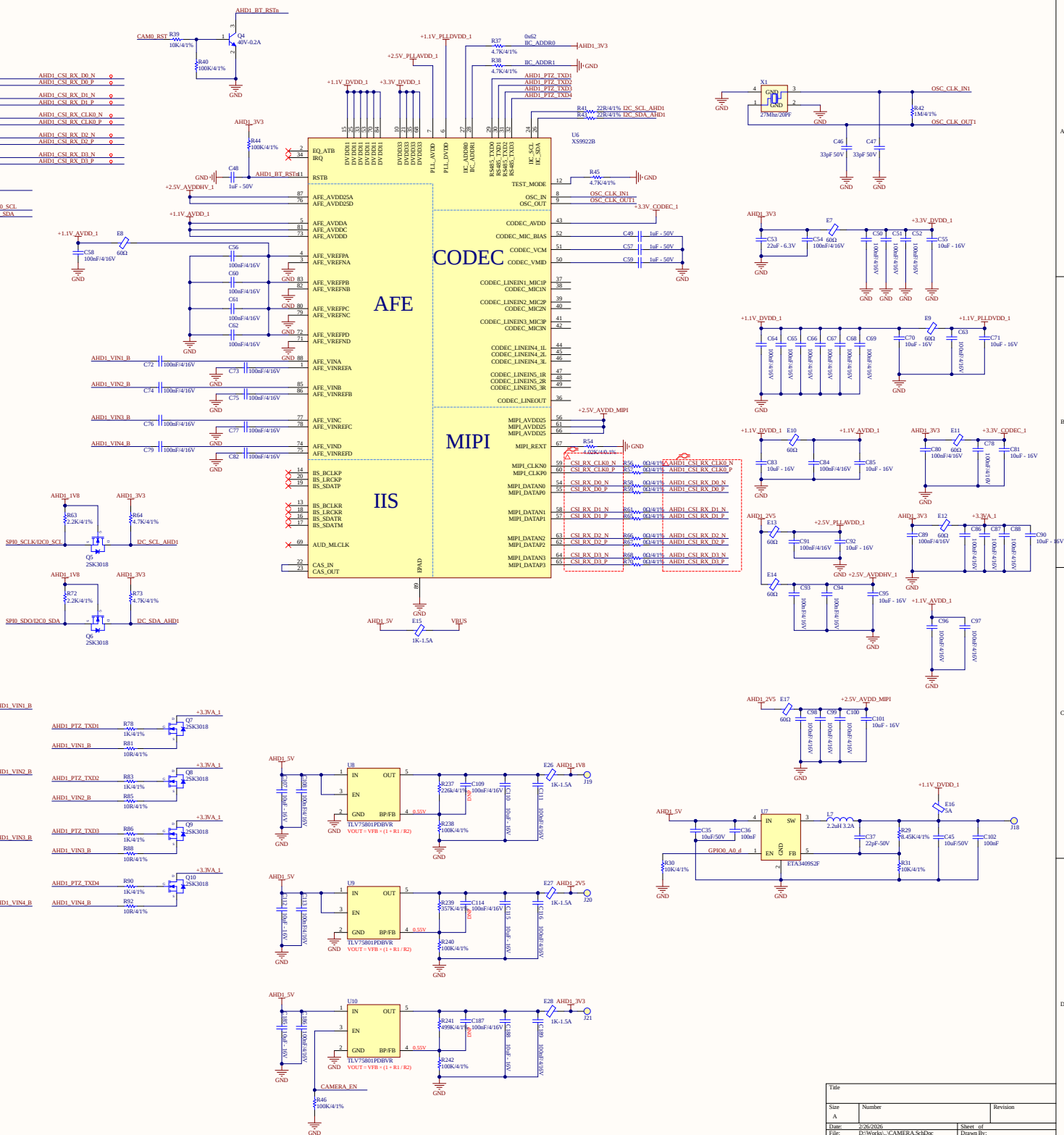
Title		
Size	Number	Revision
A4		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\MCU_RX130-140.SchDoc	Drawn By:

Đã CHECK OK
ngày 22/02/2026



Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\AUDIO.SchDoc	Drawn By:

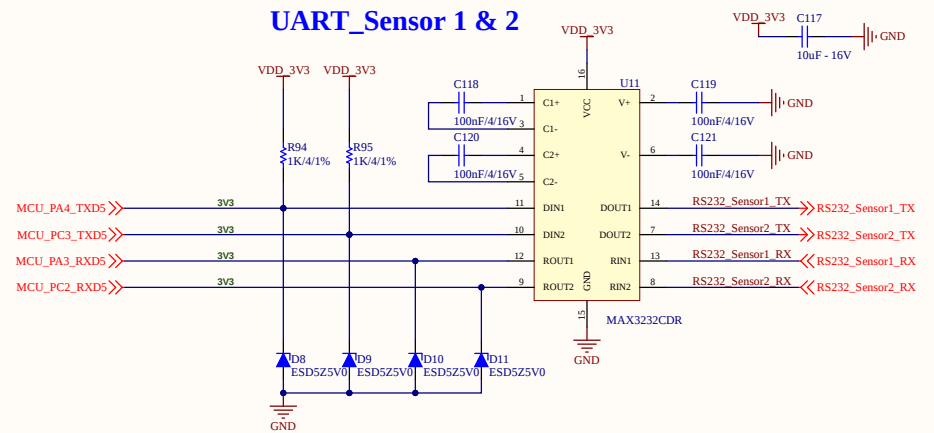
Chú ý chân
CTRL_CAM0_RST
logic 1.8V



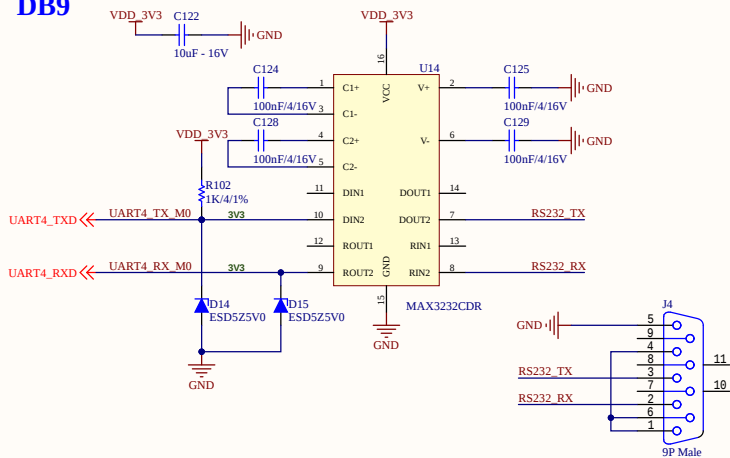
Title		
Size A	Number	Revision
Date	2/26/2026	Sheet of
File:	D:\Works\1\CAMERA\SchDoc	Drawn By:

Đã CHECK OK
ngày 26/02/2026

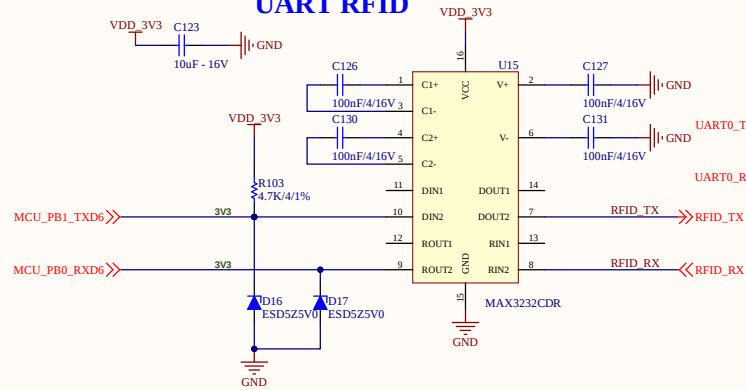
UART_Sensor 1 & 2



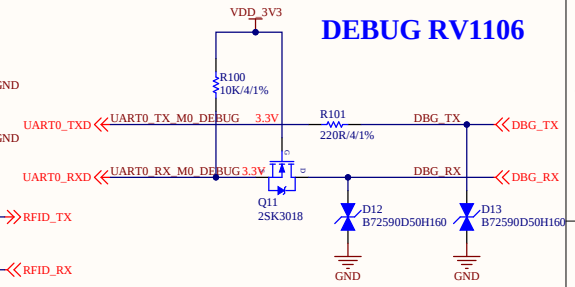
DB9



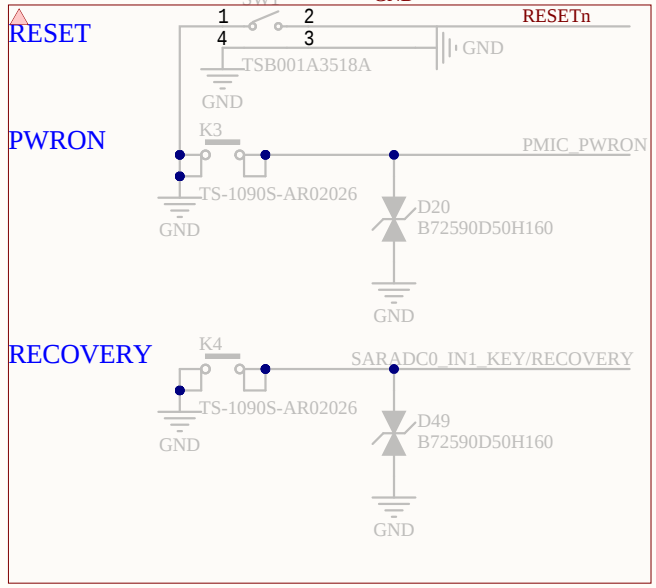
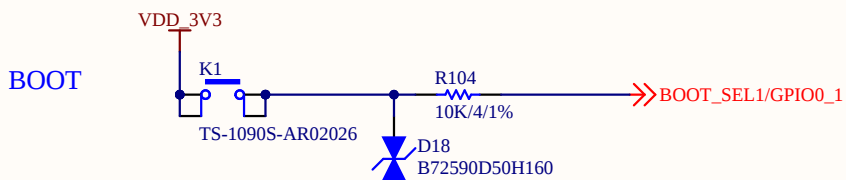
UART RFID



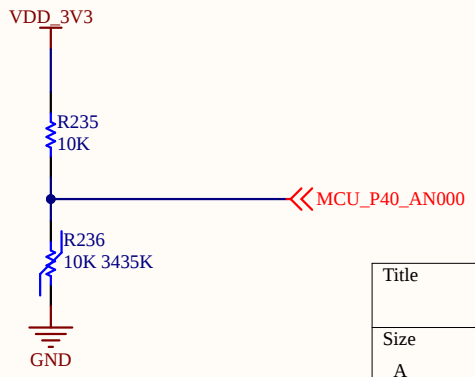
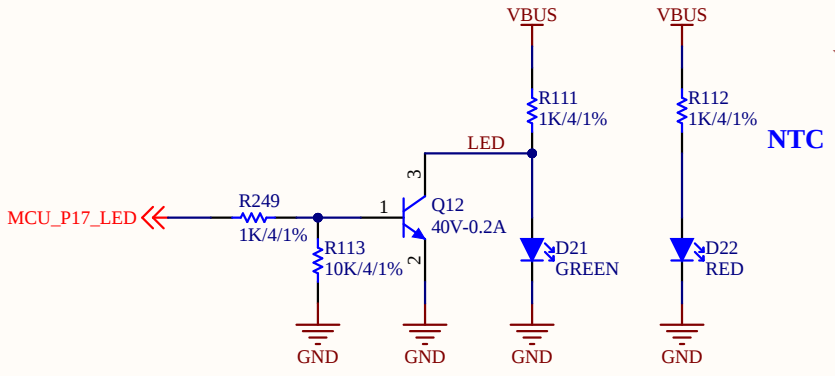
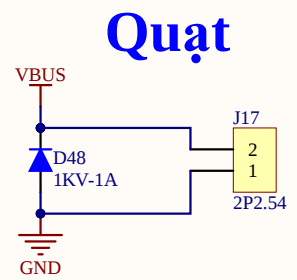
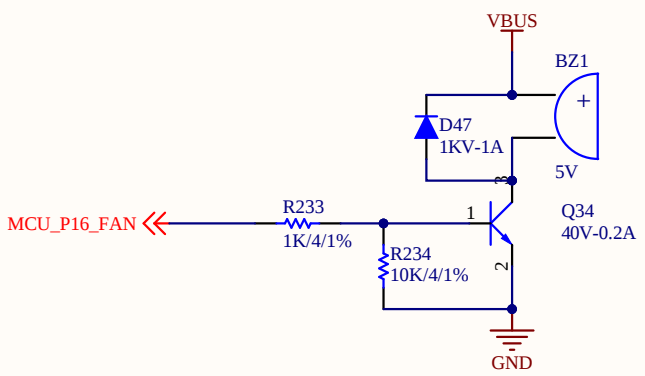
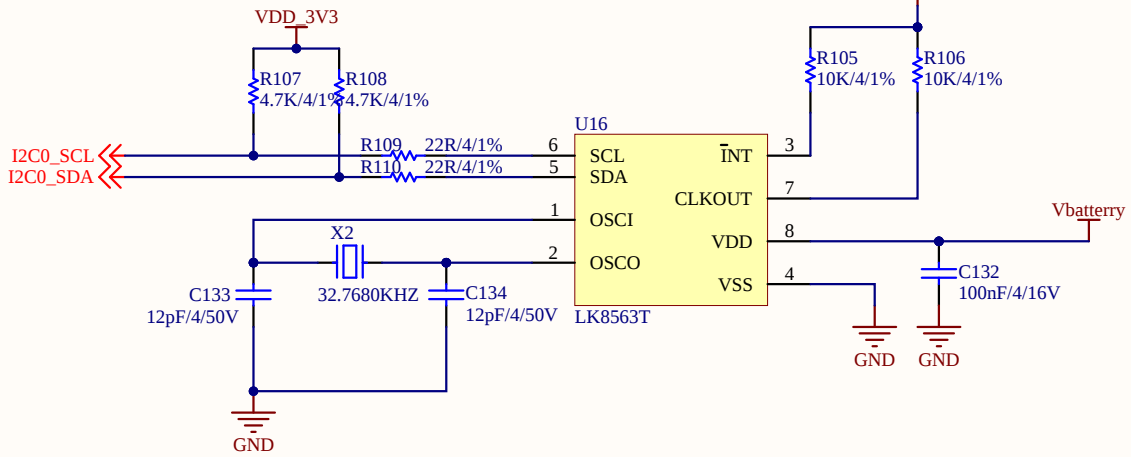
DEBUG RV1106



Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\UART.SchDoc	Drawn By:

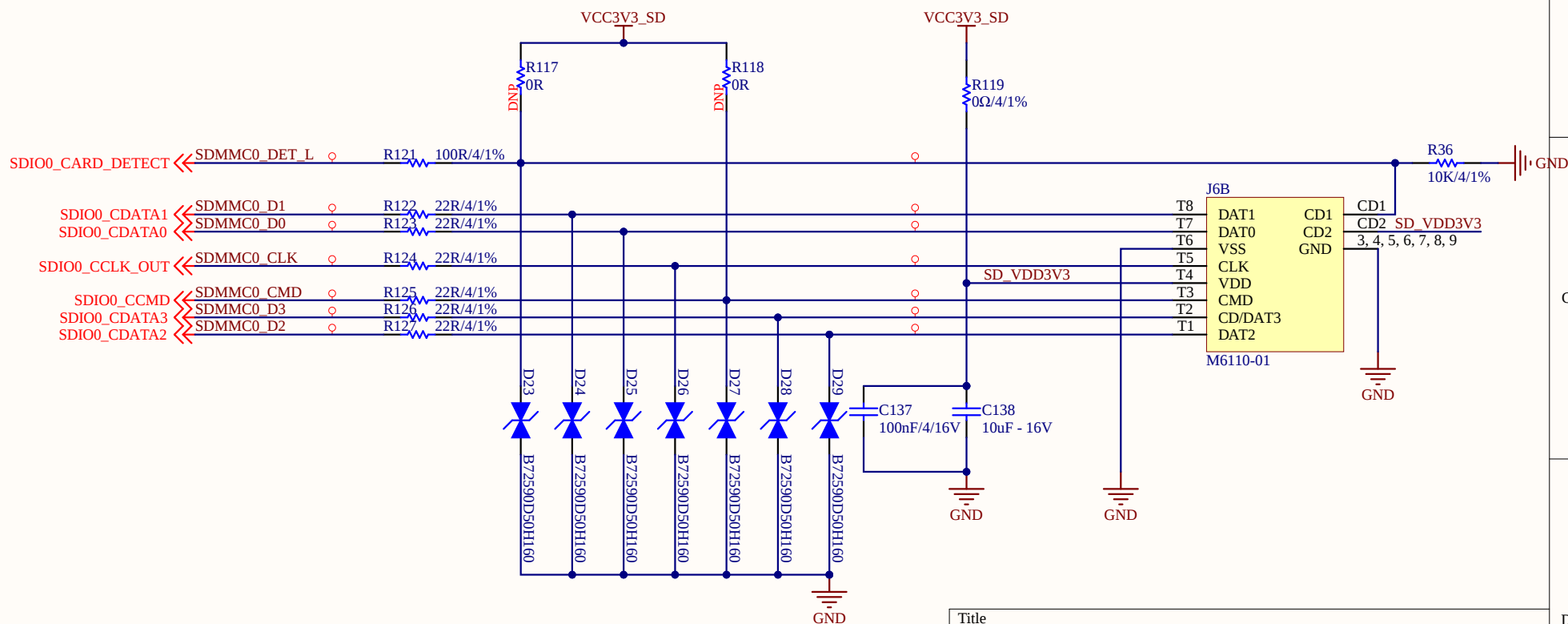
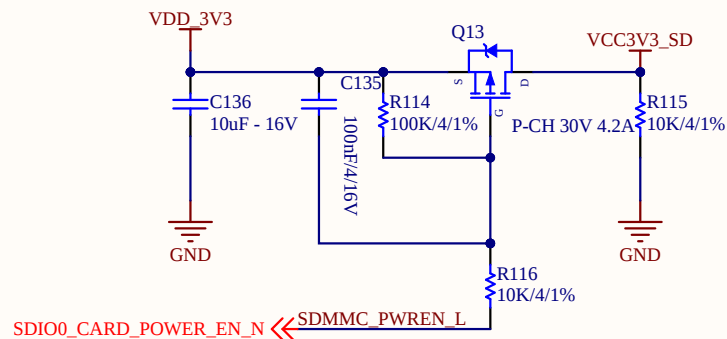


Đã CHECK OK
ngày 26/02/2026

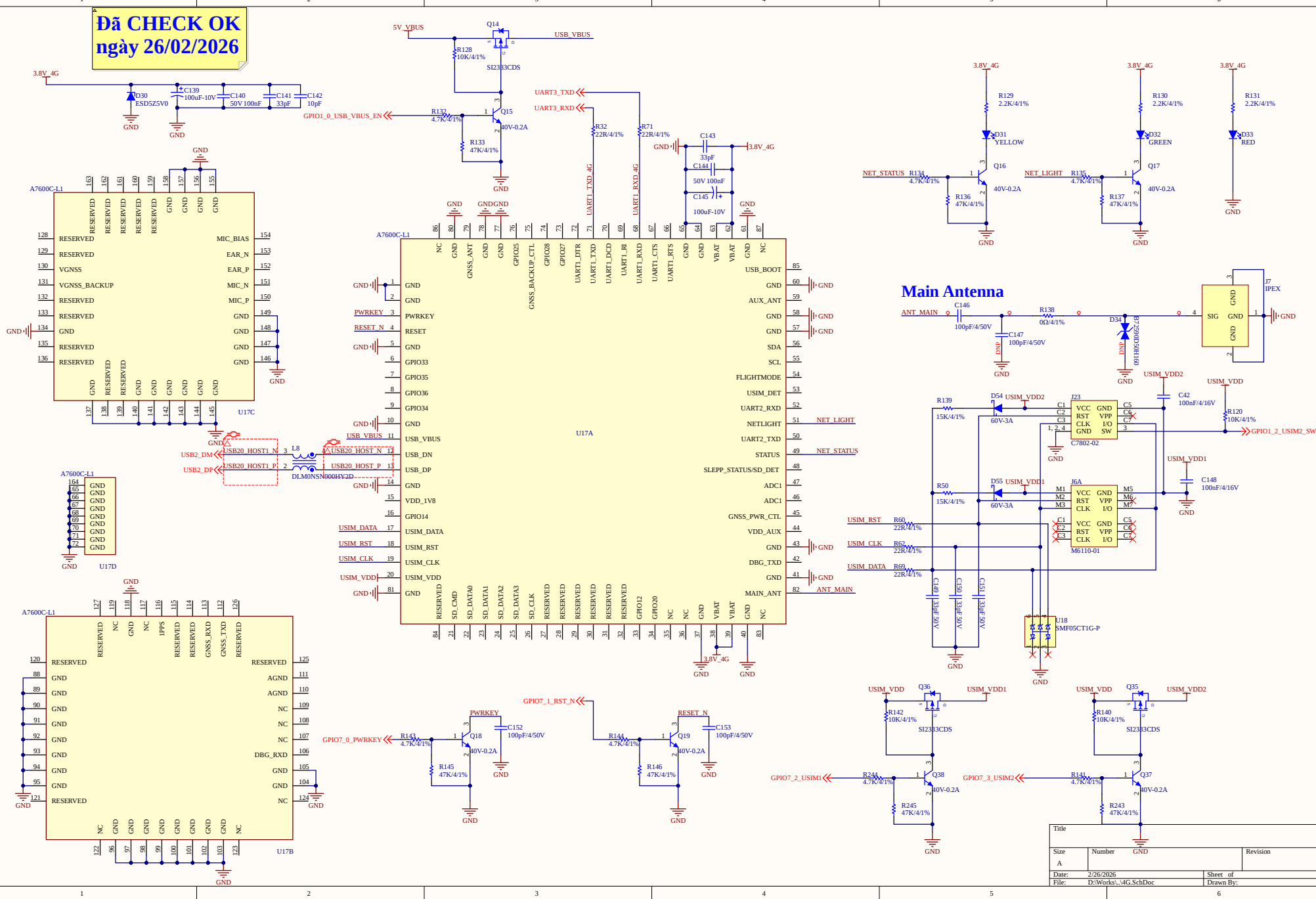


Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\Function.SchDoc	Drawn By:

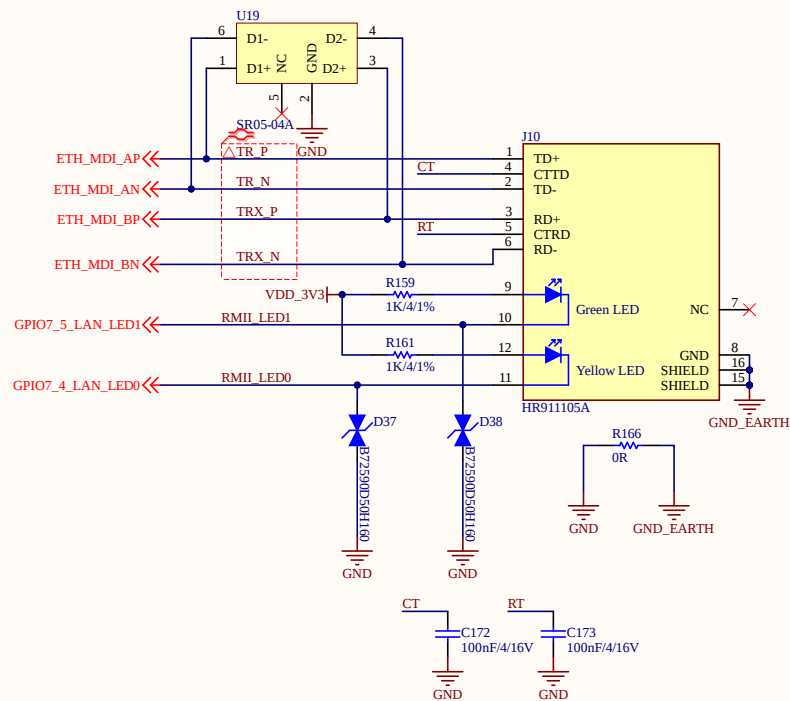
Đã CHECK OK
ngày 26/02/2026



Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\TF.SchDoc	Drawn By:



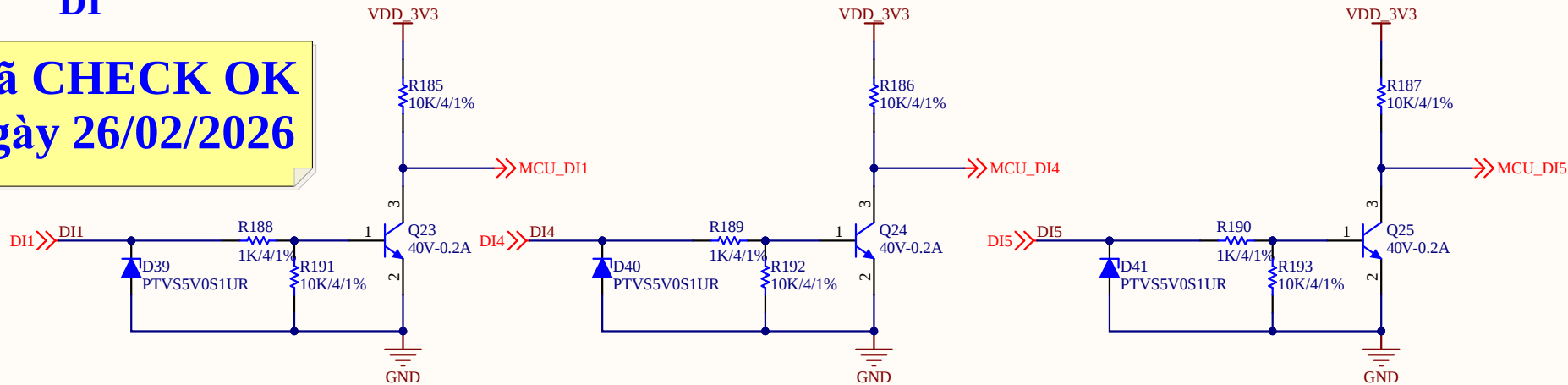
Đã CHECK OK
ngày 26/02/2026



Title		
Size A	Number	Revision
Date: 2/26/2026	Sheet of	
File: D:\Works\...\ETH.SchDoc	Drawn By:	

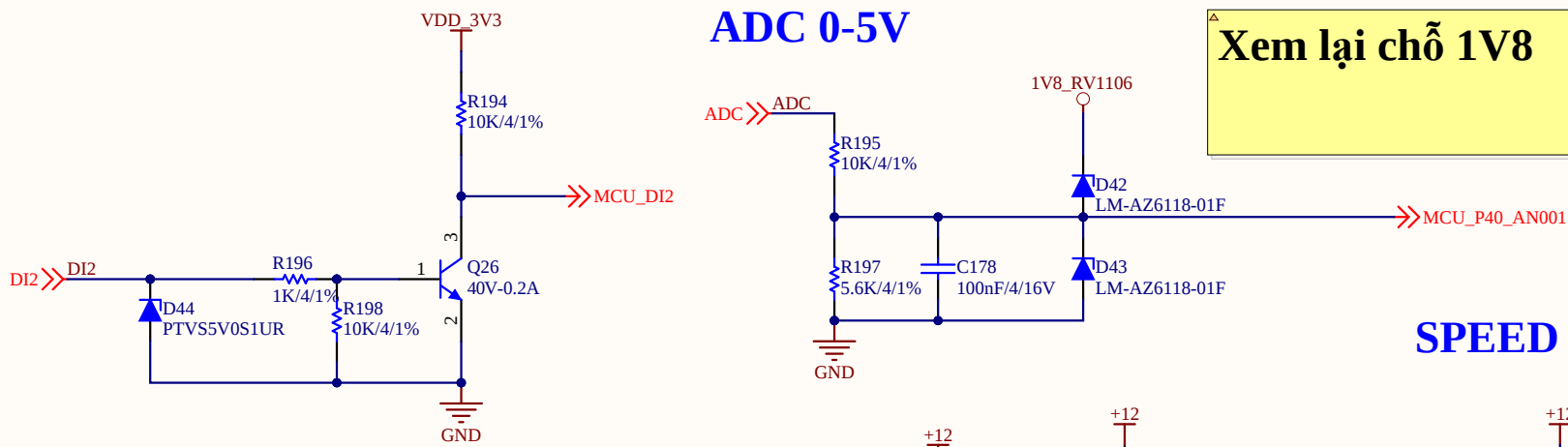
DI

Đã CHECK OK
ngày 26/02/2026

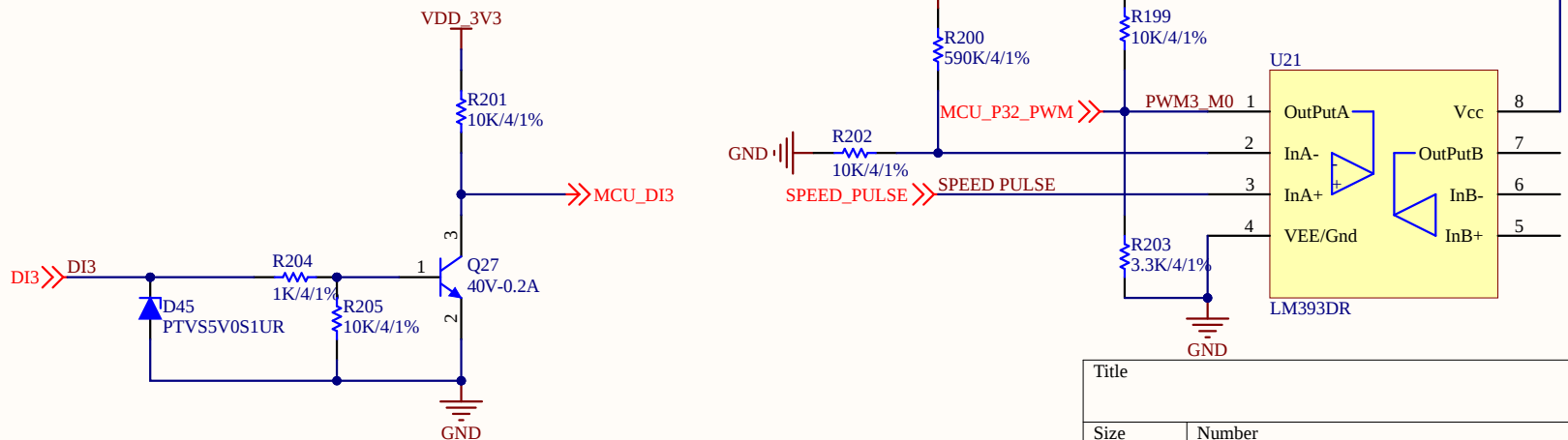


ADC 0-5V

Xem lại chỗ 1V8

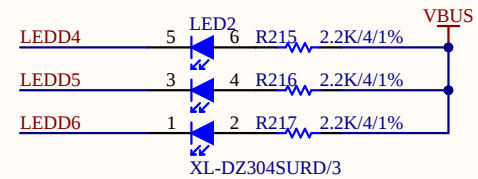
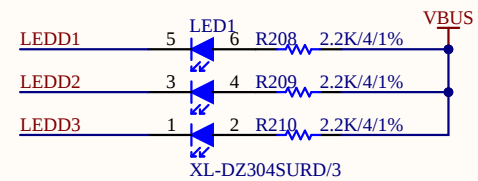
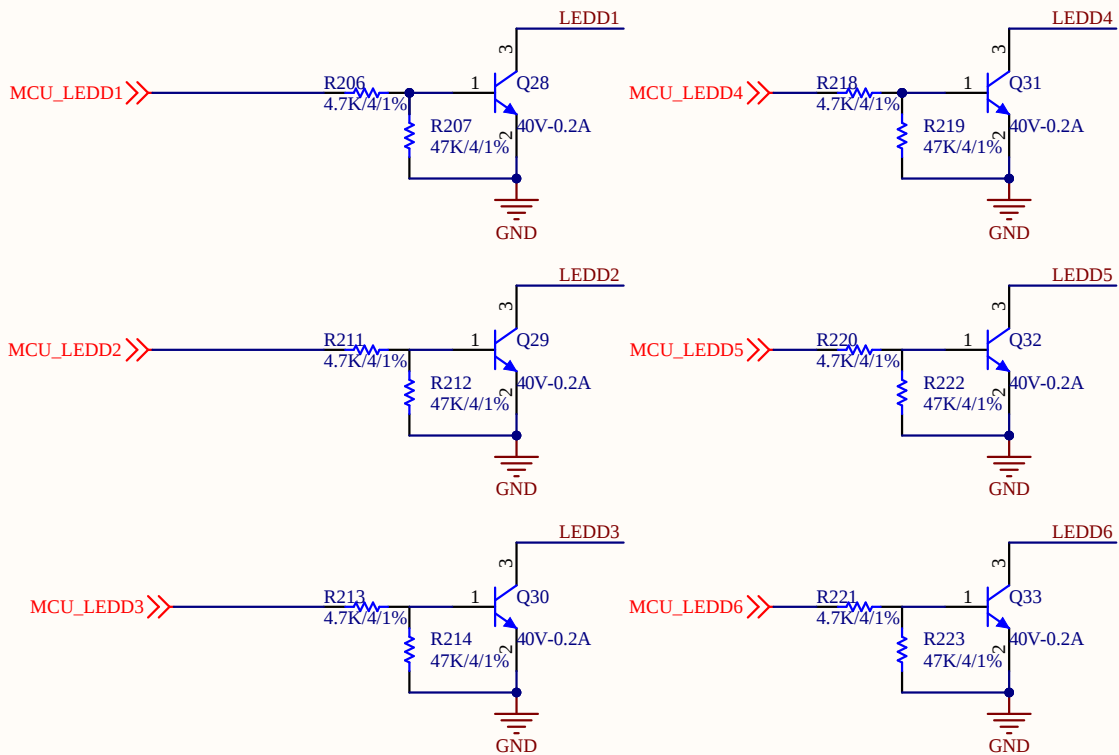


SPEED PULSE



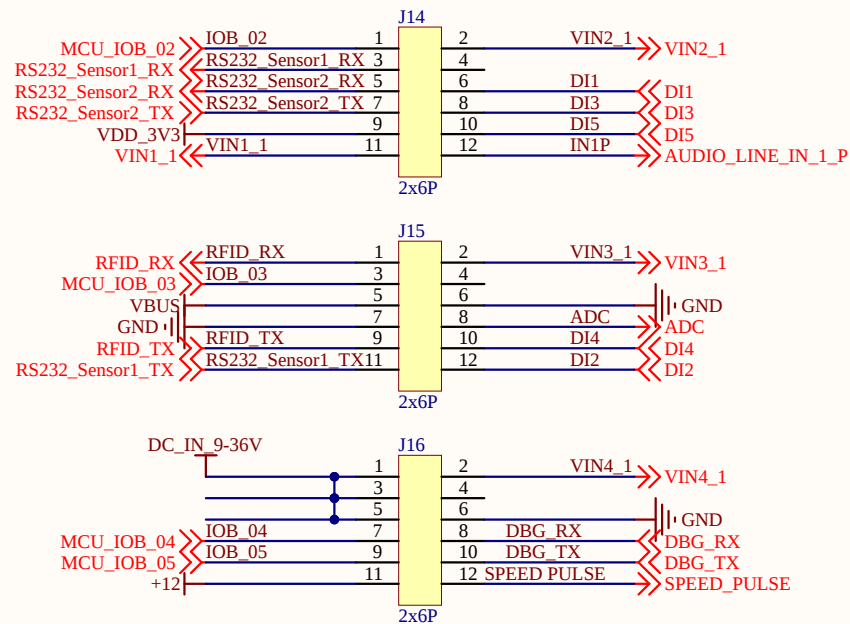
Title			
Size	Number		Revision
A	2/26/2026		Sheet of
File:	D:\Works\...\INPUT.SchDoc		Drawn By:

Đã CHECK OK
ngày 26/02/2026



Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\OUTPUT.SchDoc	Drawn By:

Đã CHECK OK
ngày 26/02/2026



Title		
Size	Number	Revision
A		
Date:	2/26/2026	Sheet of
File:	D:\Works\...\CONN.SchDoc	Drawn By: